**June 2011**


**MADALGO seminars by Nodari Sitchinava, Aarhus University**

**I/O-optimal parallel distribution sweeping for private-cache chip multiprocessors**

**Abstract:**
The talk will describe an I/O-optimal parallel algorithm for solving the orthogonal line segment intersection reporting problem in the Parallel External Memory (PEM) model. The PEM model is a parallel model that has been introduced recently to model the cache-oriented memory design of modern multicore architectures.

**Joint work with Deepak Ajwani and Norbert Zeh**